

10/535064

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<b>FORM PTO-1449/A and B (Modified)</b> <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				APPLICATION NO.: Unassigned	ATTY. DOCKET NO.: S1022.81244US00
				FILING DATE: Herewith	CONFIRMATION NO.: Unassigned
				APPLICANT: Xavier Robert	
				GROUP ART UNIT: Unassigned	EXAMINER: Unassigned
Sheet 1 of 1					

**U.S. PATENT DOCUMENTS**

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
HIEN VO		5,724,505		Argade et al.	03/03/1998
		5,848,264		Baird et al.	12/08/1998
Digitally signed by HIEN VO DN: CN = HIEN VO, C = US, OU = 2863 Reason: I have reviewed this document Date: 2006.09.28 17:59:55 -04'00'		5,974,573		Martin	10/26/1999
		6,052,774		Segars et al.	04/18/2000
		2002/0013893	A1	Roy et al.	01/31/2002

**FOREIGN PATENT DOCUMENTS**

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
HIEN VO		EP	1184790	A	Mann	03/06/02	

**OTHER ART — NON PATENT LITERATURE DOCUMENTS**

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
HIEN		International Search Report from corresponding International Application No. PCT/FR2002/003996, filed November 21, 2002	
VO		International Search report from a related International Application No. PCT/FR2002/003908, filed November 14, 2002	
Digitally signed by HIEN VO DN: CN = HIEN VO, C = US, OU = 2863 Reason: I have reviewed this document Date: 2006.09.28 18:00:41 -04'00'		International Search report from a related International Application No. PCT/FR2002/003909, filed November 14, 2002	
		Nexis 5001 Forum: Standard for a Global Embedded Processor Debug Interface, December 15, 1999, IEEE-ISTO XP002247195	

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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_\_, filed \_\_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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